

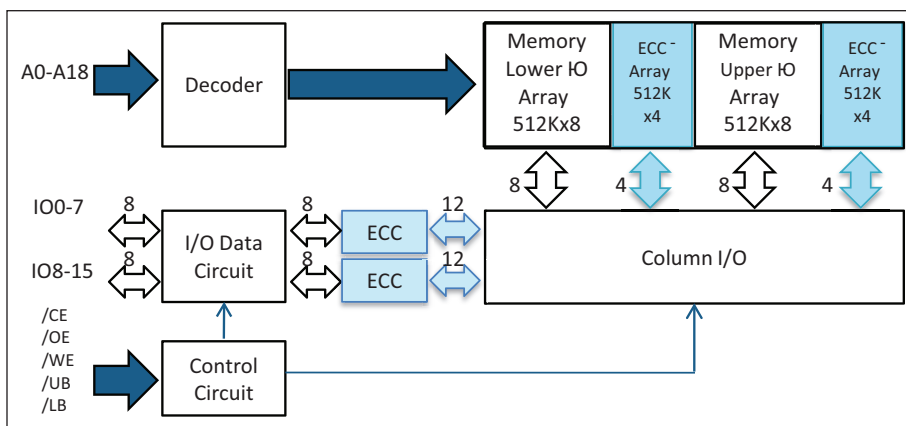


8Mb High Speed Low Power Asynchronous SRAM with Error Correction Code (ECC)

ISSI's latest Error Correction based 8Mb High Speed Low Power Asynchronous SRAM is in production. This innovative design reinforces ISSI's long-term commitment to SRAMs with the highest quality and performance. This industry's first Error Correction Code (ECC) based Asynchronous SRAM meets high quality requirements in automotive, industrial, military-aerospace, and other applications.

Error Detection and Error Correction

- Independent ECC with hamming code for each byte
- Detect and correct one bit error per each byte
- Better reliability than parity code schemes which can only detect an error but not correct an error
- Backward Compatible: Drop in replacement to current in industry standard devices (without ECC)



► Applications

- Automotive
- Military-Aerospace/Medical
- Industrial
- Telecom/Networking

► Additional ECC Async SRAMs

- 2Mb, 4Mb, 1Mb

Key Features

	IS64WV51216EDBLL (A1)	IS64WV51216EDBLL (A3)	Comments
Temperature Support	Industrial (-40°C to +85°C)	Automotive (-40°C to +125°C)	Contact ISSI for military temperature
Technology	65nm	65nm	
Standby Current	15mA	35mA	Typical value 2mA
Operating Current	50mA	65mA	Typical value 15 mA
Data Retention Current	15mA	35mA	Typical value 2mA
Packaging	TSOP-II (44 pins) BGA (48 pins)	TSOP-II (44 pins) BGA (48 pins)	Pin compatible with industry standard 8Mb Async. SRAM
Speed	10ns	10ns	
Copper Leadframe	Yes	Yes	Improved thermal performance
Lead-free and Leaded	Yes	Yes	RoHS Compliant
Availability	Production	Production	